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1. Field of the Invention
The present invention relates to a MOS (Metal Oxide Semiconductor) device, and more particularly to a MIS (Metal Insulator Semiconductor) device or MOSFET (Metal Oxide Semiconductor Field Effect Transistor) of high breakdown voltage.

2. Description of the Prior Art
A conventional MOSFET of high breakdown voltage is shown in FIG. 4. In FIG. 4, a P-type substrate 10 with a P-type base diffusion region 12 on the upper surface thereof is provided on which an N.sup.+ -type source region 14 and N.sup.+ -type drain region 16 are provided to be spaced from the P-type base diffusion region 12 with a predetermined channel forming region between them and which are covered with a gate insulation layer 18, and further on which an N-type source-drain extension layer 20 having the same conductivity type as that of the N.sup.+ -type drain region 16 is provided. The N-type source-drain extension layer 20 is formed within the periphery of the upper surface of the N.sup.+ -type drain region 16 so that it will not contact the P-type base diffusion region 12. A source electrode 22 is formed on the P-type substrate 10 and N-type source-drain extension layer 20 to contact the N.sup.+ -type source region 14 and N-type source-drain extension layer 20, respectively. Drain electrode 24 is formed on the N.sup.+ -type drain region 16. The conventional MOSFET having such a structure is not high in the gate breakdown voltage. In other words, the PN junction between the P-type substrate 10 and P-type base diffusion region 12, the PN junction between the P-type base diffusion region 12 and N-type source-drain extension layer 20, and the PN junction between the N-type source-drain extension layer 20 and N-type drain region 16 are the first breakdown points. Also, the PN junction between the P-type base diffusion region 12 and N-type source-drain extension layer 20 is the second breakdown point. Further, the PN junction between the N-type source-drain extension layer 20 and N-type drain region 16 is the third breakdown point. In the conventional MOSFET



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